

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,282,761 B2
APPLICATION NO. : 10/696615
DATED : October 16, 2007
INVENTOR(S) : Ahn et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

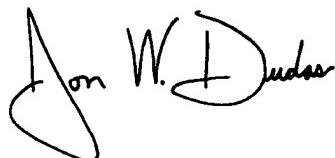
On Title Page:

Sec. (54): Should read --SEMICONDUCTOR MEMORY DEVICES HAVING
OFFSET TRANSISTORS--

Column 1, lines 1-3: Please correct title to read --SEMICONDUCTOR MEMORY
DEVICES HAVING OFFSET TRANSISTORS--

Signed and Sealed this

Twenty-fifth Day of December, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office